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S/N 10/756961

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	SHIBATA	Examiner:	H. Pham
Serial No.:	10/756961	Group Art Unit:	2814
Filed:	January 13, 2004	Docket No.:	10873.0871USD1
Title:	SEMICONDUCTOR MEMORY DEVICE WITH TRENCH-TYPE STACKED CELL CAPACITORS AND METHOD FOR MANUFACTURING THE SAME		

CERTIFICATE UNDER 37 CFR 1.600I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on June 1, 2006By: Carrie Vanderhinde
Name: Carrie Vanderhinde

Mail Stop: AF
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

RESPONSE UNDER 37 CFR 1.116

Dear Sir:

In response to the final Office Action mailed March 1, 2006, Applicant responds as follows:

Remarks begin on page 2 of this document.